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ATTENTION: BOX AFTER FINAL
RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE REQUESTED
EXAMINING GROUP 2814

PATENT
Customer No. 22,852
Attorney Docket No. 04329.2613

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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TECHNOLOGY CENTER 2800

In re Application of:)
)
Katsuhiko HIEDA) Group Art Unit: T. Le
)
Application No.: 09/916,509) Examiner: 2814
)
Filed: July 30, 2001)
)
For: FIELD EFFECT TRANSISTOR)
USING SIDE-WALL GATES TO)
PREVENT PUNCH-THROUGH)
(As Amended))

Commissioner for Patents
Washington, DC 20231

Sir:

REQUEST FOR APPROVAL OF DRAWING CHANGES

Subject to the approval of the Examiner, it is respectfully requested that Figs. 79-81 in the above-captioned application be amended by adding the legend "Prior Art".

The changes are indicated in red on the attached copies of the originally filed drawings.

Upon approval of the proposed changes, Applicant respectfully requests that the submission of revised drawings be deferred until after a notice of allowance has issued.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

By: Richard V. Burgujian Reg 24,014
for Richard V. Burgujian
Reg. No. 31,744

Dated: December 24, 2002

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